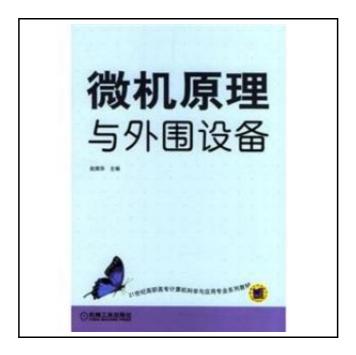# Microcomputer Principle and peripherals [Paperback]

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MICROCOMPUTER PRINCIPLE AND PERIPHERALS [PAPERBACK]

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paperback. Book Condition: New. Ship out in 2 business day, And Fast shipping, Free Tracking number will be provided after the shipment.Paperback Pages Number: 242 Language: Simplified Chinese Publisher: Machinery Industry Press; 1st edition (January 1. 2004). Microcomputer Principle and peripherals. the system describes the application of the basic principles of micro-computer and peripheral equipment . The book is 11 chapters. including the concept of micro-computer and bus. microprocessor. internal memory. the assembly language instructions and programming methodology. interrupts and its applications. the input and output interface technology. human-machine interface and peripherals. The book concept is clear. step-by-step. the actual explanation. with a strong practical. Computer Architecture and peripherals can think vocational college computers and related professional materials. reference books will serve as a micro-computer application system design and development staff. Contents: Imprint Preface Chapter 1 Introduction 1.1 micro-computer development 1.2 micro-computer system. basic components 1.3 Summary 1.4 Exercises Chapter 2 microprocessor 2.1 microprocessor basic structure of the 2.2 typical microprocessor 2.3 80X86 microprocessor 2.4 Pentium microprocessor 2.5 Summary 2.6 Exercises Chapter 3. the internal memory 3.1 is stored along with the storage system Overview 3.2 Main Memory 3.3 Cache Memory 3.4 Virtual Memory 3.5 PC series machines in main memory 3.6 Summary 3.7 Exercises Chapter 4 Instruction Set Programming 4.2 Instruction Set 4.1 Instruction format and addressing 4.3 assembly language program design based on the 4.4 assembly language programming 4.5 Summary 4.6 Exercises Chapter 5 Bus 5.1 Bus Overview 5.4 Summary 5.2 5.3 commonly used bus standard bus based on the system hardware extensions 5.5 6.1 of the exercises in Chapter 6 interrupt system interrupt Overview 6.2 interrupt structure 6.3 80.888.086 7.1 IO interface interrupt processing 6.4 Interrupt Controller 6.5 Summary 6.6 Exercises Chapter 7 Input and Output Interface Technology Overview 7.2 IO Port addressing a 7.3 IO data...

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